Patent Attorney's Docket No. <u>026350-028</u>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Toshiro HIRAMOTO et al.

Application No.: 09/389,321

Filed: September 3, 1999

For: MOS TRANSISTOR WITH A

CONTROLLED THRESHOLD

**VOLTAGE** 

Group Art Unit: 2826/

Examiner: T. Dickey

RESPONSE UNDER 37 C.F.R. §1.116

Assistant Commissioner for Patents

Washington, D.C. 20231

Date: April 19, 2002

Sir:

This response responds to the Office Action October 19, 2001 (Paper No. 11).

Concurrently filed with this response is a Petition for Extension of Time for a Three Month Period and a Notice of Appeal.

## **REMARKS**

This response responds to the Office Action dated October 19, 2001 in which the Examiner rejected claims 1-12 under 35 U.S.C. §103.

Applicants acknowledge the objections to the drawings by the PTO draftsperson.

The formal drawings will be prepared upon receipt of a Notice of Allowance.

Concurrently filed with this response are copies of four documents providing definitions of a EIB and VTMOS. In particular, Appendix 1 shows an article by the inventors defining an EIB. Appendix 2 is an article by *Kuroda et al.* in which a VTCMOS